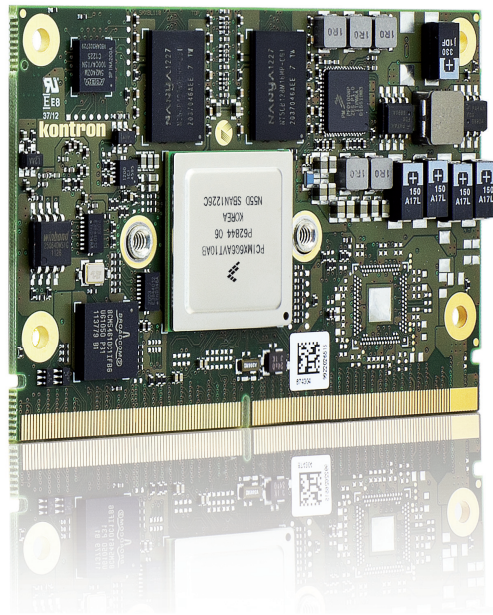


» SMARC-sAMX6i «



SMARC - Smart Mobile Architecture

- » Ultra low power COM based on Freescale™ i.MX 6 Family
- » SoC scalable from Solo Core up to Quad Core
- » Commercial and industrial temperature versions

Vertrieb durch:

HY-LINE®
COMPUTER COMPONENTS

Inselkammerstr.10
82008 Unterhaching
Tel: +49 89 614 503 40
www.hy-line.de/computer

SMARC-sAMX6i

Technical Information	
Processor	Freescale i.MX 6 Solo, Dual and Quad Core ARM Cortex-A9 800 MHz, 1.0 Ghz, 1.2GHz
Graphics	Dual Display HD 1080p encode and decode 2D and 3D acceleration
Memory	DDR3 up to 2 GB, memory down
Flash	Up to 64GB NAND/eMMC on-module (Custom Option)
Ethernet	10/100/1000 Mbit/sec
USB	3x USB 2.0 port (one OTG)
Display	Parallel LCD 18/24 bit LVDS Single Channel 18/24bit HDMI
Image Capture Interfaces	2 Interfaces (PCAM, CSI)
Serial	2x RX/TX (Ser1/3); 2x UART (Ser0/2)
Additional Interfaces	Up to 3 PCIe x1, MLB150, 12xGPIOs, SDIO, SATA eMMC, 2x SPI, 5x I2C, 2x I2S, SPDIF, WDT, 2x CAN, Battery and System Management
SW Support	Windows WEC7 Android Linux
Power	tbd
Thermal	Commercial temperature: 0°C to +60°C and Industrial temperature: -40°C to 85°C (800MHz versions)
Power Supply	3V to 5.25V - Operates directly from single level Lithium Ion cells, or fixed 3.3V or 5V power supplies
Compliance	Form Factor: 82 mm x 50 mm as defined by the ULP-COM specification Compliance: SMARC (Smart Mobile Architecture) specification by SGET

Ordering Information		
Article*	Part No.	Description
ULP-COM-sAMX6i 0.8 512/4GB	51003-0540-08-1	SMARC with Freescale i.MX6 Solo, 800MHz single core, 512MB DRAM memory down, 4GB onboard Flash, industrial temperature, no SATA support
ULP-COM-sAMX6i 2x0.8 1/4GB	51003-1040-08-2	SMARC with Freescale i.MX6 Dual, 800MHz dual core, 1GB DRAM memory down, 4GB onboard Flash, industrial temperature, with SATA support
ULP-COM-sAMX6i 4x0.8 1/4GB	51003-1040-08-4	SMARC with Freescale i.MX6 Quad, 800MHz quad core, 1GB DRAM memory down, 4GB onboard Flash, industrial temperature, with SATA support
*Please ask for more SoC or memory configurations.		

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