

# ***WXGA-TFT-PCAP-Modul Datenblatt***

Modell SCF0507927GGU01

## **Kurzdaten**

Hersteller	Data Image
Diagonale	5,7" / 14,5 cm
Format	normal
Auflösung	640 x 480
Backlight	LED / 765 cd/m <sup>2</sup>
Interface	RGB
Touchscreen	PCAP
Temperatur	-20... +70°C (Betrieb)

Vertrieb durch:



Confidential Document

# DATA IMAGE CORPORATION

## CTP Module Specification

Preliminary

ITEM NO.: SCF0507927GGU01

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### 3. APPLICATION

DVD player, Car TV, UMPC, POS

### 4. GENERAL SPECIFICATIONS

Parameter	Specifications	Unit
Screen Size	5.7 (diagonal)	inch
Display Format	640(H) x (R,G,B) x 480(V)	dot
LCD Active Area	115.2(W) x 86.4(H) mm	mm
Dot Pitch	0.06(W) x 0.18(H) mm	mm
Pixel Configuration	R.G.B. Stripe	
Outline Dimension	142.75(W) x 113.95(H) x 7.8(D)	mm
CTP Surface treatment	Clear	
Back-light	LED	
Weight	147(typ)	g
View Angle direction (Gray inversion)	12 o'clock	

### 5. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	MIN.	MAX.	Unit	Remark
Power supply voltage	V <sub>CC</sub>	-0.3	5.0	V	
Logic input voltage	V <sub>I</sub>	-0.3	V <sub>CC</sub> +0.3	V	
Operating temperature	T <sub>OP</sub>	-20	+70	°C	Ambient temperature
Storage temperature	T <sub>ST</sub>	-30	+80	°C	Ambient temperature

### 6. ELECTRICAL CHARACTERISTICS

V<sub>SS</sub>=0V, DCLK=25MHz, T<sub>a</sub>=25°C

Parameter	Symbol	MIN.	Typ.	MAX.	Unit	Remark
Power Supply voltage for LCD	V <sub>CC</sub>	+3.0	+3.3	+3.6	V	
Power Supply Current for LCD	I <sub>CC</sub>	--	111	140	mA	V <sub>CC</sub> =3.3V
Power Supply voltage for LED	V <sub>LED</sub>	4.5	5	5.5	V	
Power Supply Current for LED	I <sub>LED</sub>		333	400	mA	V <sub>LED</sub> =5.0V
Ripple voltage	V <sub>RF</sub>	--	--	100	mV <sub>P-P</sub>	
"H" level logical input voltage	V <sub>IH</sub>	0.7V <sub>CC</sub>	--	V <sub>CC</sub>	V	
"L" level logical input voltage	V <sub>IL</sub>	0	--	0.3V <sub>CC</sub>	V	
ADJ frequency		19K	20K	21K	Hz	
ADJ input voltage	V <sub>IH</sub>	3.0	--	3.3	V	
	V <sub>IL</sub>	0	--	0.3	V	
LED Dice life time		--	50000	--	Hr	Note 1

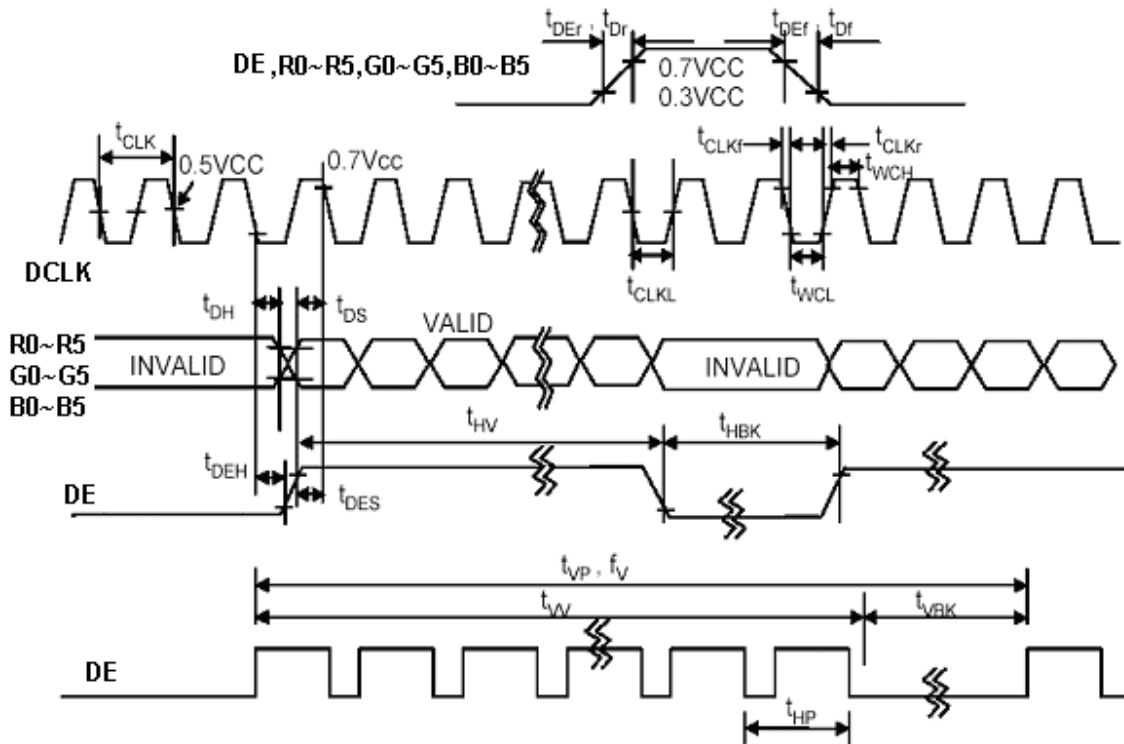
Note 1: The "LED dice life time" is defined as the brightness decrease to 50% original brightness that the ambient temperature is 22 °C and LED dice current=20mA.

## 7. INPUT SIGNAL CHARACTERISTICS

### 7.1 DE mode Input signal characteristics

Signal	Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Remarks
DCLK	Period	$t_{CLK}$	33	40	43	ns	
	Frequency	$f_{CLK}$	23	25	30	MHz	
	Low Level Width	$t_{WCL}$	6	-	-	ns	
	High Level Width	$t_{WCH}$	6	-	-	ns	
	Rise, Fall Time	$t_{CLKr}, t_{CLKf}$	-	-	3	ns	
	Duty <sup>(1)</sup>	-	0.45	0.50	0.55	-	
DE (Data Enable)	Setup Time	$t_{DES}$	5	-	-	ns	
	Hold Time	$t_{DEH}$	10	-	-	ns	
	Rise, Fall Time	$t_{DEr}, t_{DEf}$	-	-	16	ns	
	Horizontal Period	$t_{HP}$	750	800	900	$t_{CLK}$	
	Horizontal Valid	$t_{HV}$	640	640	640	$t_{CLK}$	
	Horizontal Blank	$t_{HBK}$	110	160	260	$t_{CLK}$	
	Vertical Period	$t_{VP}$	515	525	560	$t_{HP}$	
	Vertical Valid	$t_{W}$	480	480	480	$t_{HP}$	
	Vertical Blank	$t_{VBK}$	35	45	80	$t_{HP}$	
	Vertical Frequency	$f_v$	55	60	65	Hz	
Data R,G,B	Setup Time	$t_{DS}$	5	-	-	ns	
	Hold Time	$t_{DH}$	10	-	-	ns	
	Rise, Fall Time	$t_{Dr}, t_{Df}$	-	-	3	ns	

Note: (1)  $t_{CLKL} / t_{CLK}$ .

**7.1.1 DE mode timing waveform**


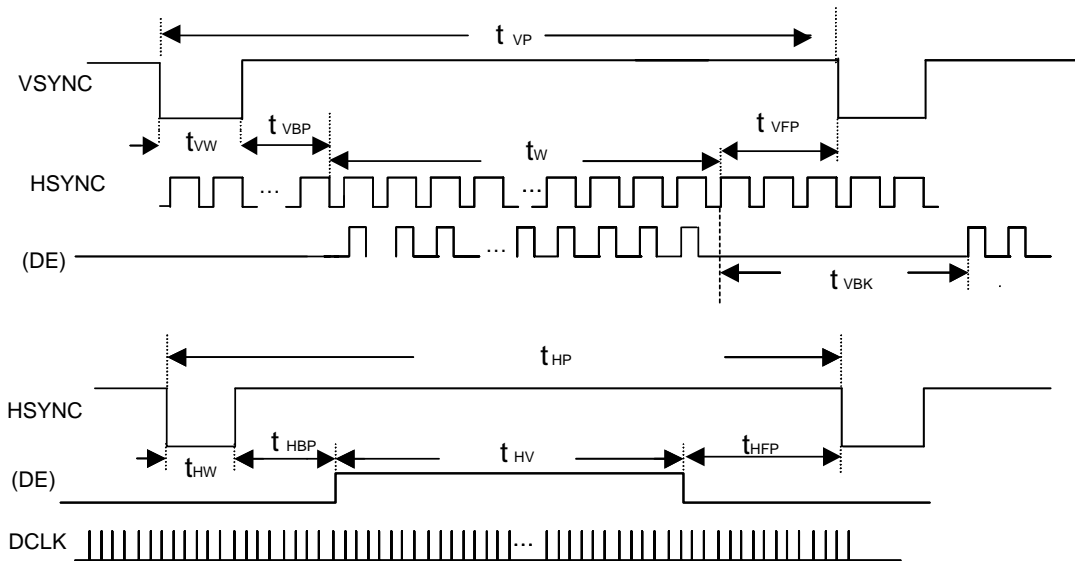
## 7.2 SYNC mode Input signal characteristics

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Remarks
Clock Period	$t_{CLK}$	33	40	43	ns	
Clock Frequency	$f_{CLK}$	23	25	30	MHz	
Clock Low Level Width	$t_{WCL}$	6	-	-	ns	
Clock High Level Width	$t_{WCH}$	6	-	-	ns	
Clock Rise, Fall Time	$t_{CLKr}, t_{CLKf}$	-	-	3	ns	
HSYNC Period	$t_{HP}$	750	800	900	$t_{CLK}$	
HSYNC Pulse Width	$t_{HW}$	5	30	-	$t_{CLK}$	
HSYNC Front Porch	$t_{HFP}$	1	16	116	$t_{CLK}$	
HSYNC Back Porch	$t_{HBP}$	1	114	139	$t_{CLK}$	
HSYNC Width + Back Porch	$t_{HW} + t_{HBP}$	144	144	144	$t_{CLK}$	
Horizontal Blank	$t_{HBK}$	1	160	260	$t_{CLK}$	
Horizontal Valid	$t_{HV}$	640	640	640	$t_{CLK}$	
VSYNC Period	$t_{VP}$	515	525	560	$t_{HP}$	
VSYNC Pulse Width	$t_{VW}$	1	3	5	$t_{HP}$	
VSYNC Front Porch	$t_{VFP}$	1	10	45	$t_{HP}$	
VSYNC Back Porch	$t_{VBP}$	30	32	34	$t_{HP}$	
VSYNC Width + Back Porch	$t_{VW} + t_{VBP}$	35	35	35	$t_{CLK}$	
Vertical Blank	$t_{VBK}$	35	45	80	$t_{HP}$	
Valid data Width	$t_{W}$	480	480	480	$t_{HP}$	
Data Setup Time	$t_{DS}$	5	-	-	ns	
Data Hold Time	$t_{DH}$	10	-	-	ns	

Note: (1)  $t_{HBK} = t_{HFP} + t_{HW} + t_{HBP}$

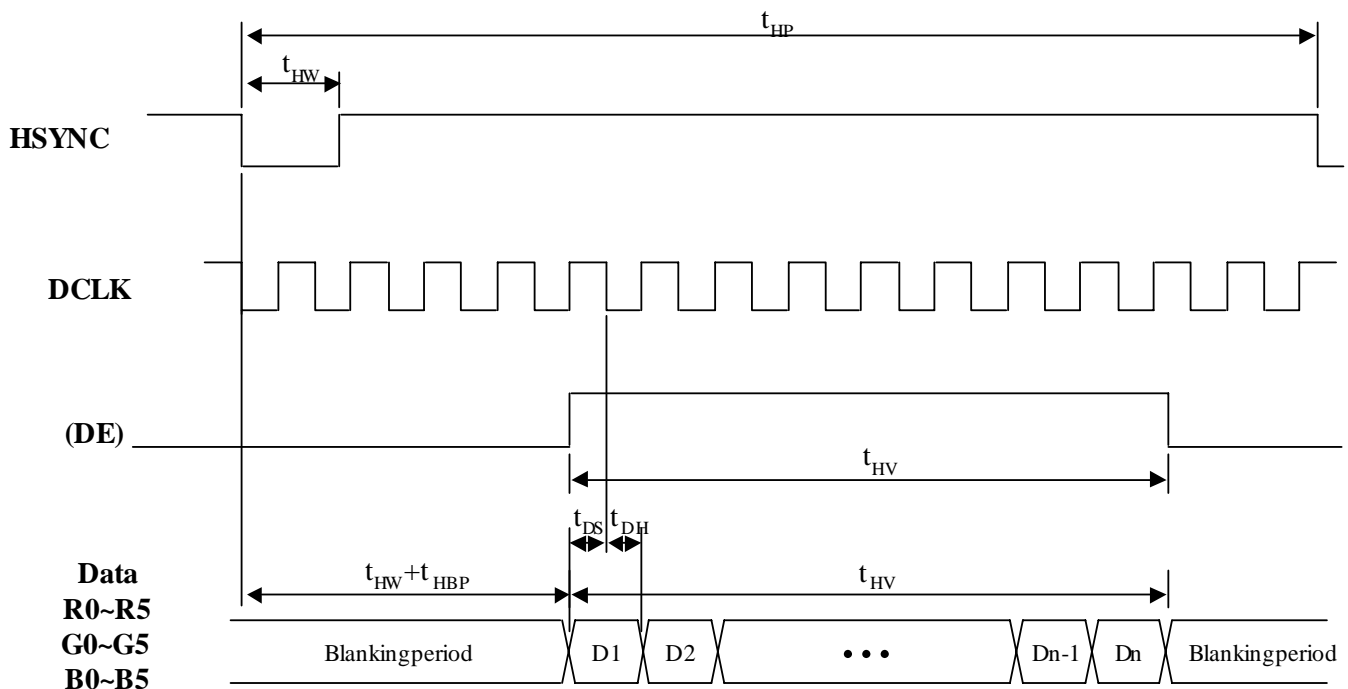
## 7.2.1 SYNC mode timing waveform

### 7.2.1.1 Input vertical timing



Remark : If SYNC mode is used, please fix DE signal to low, DE timing waveform is for reference only.

### 7.2.1.2 Input horizontal timing



Remark: If SYNC mode is used, please fix DE signal to low, DE timing waveform is for reference only.



**7.3 Color Data Assignment**

COLOR	INPUT	R DATA						G DATA						B DATA					
		DATA	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1
		MSB					LSB	MSB					LSB	MSB					LSB
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Remarks:(1) Definition of Gray Scale  
 color(n):n is series of Gray Scale  
 The more n value is, the bright Gray Scale.  
 (2)Data:1-High,0-Low

**Correspondence between Data and Display Position**

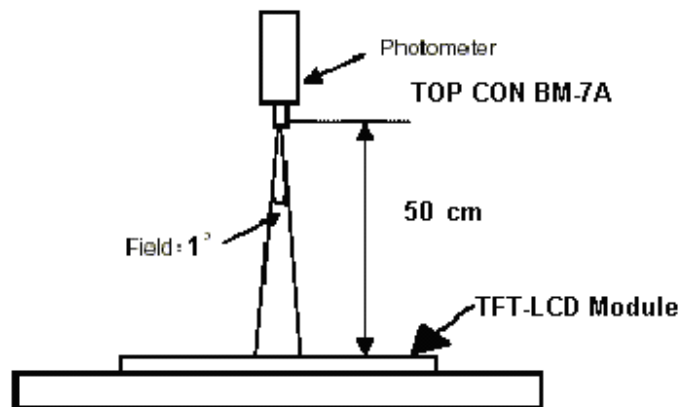
	S0001	S0002	S0003	S0004	S0005	S0006	S0007	S0008	-----	S1919	S1920
C001	R001	G001	B001	R002	G002	B002	R003	G003		G640	B640
C480	R001	G001	B001	R002	G002	B002	R003	G003		G640	B640

### 8. OPTICAL CHARACTERISTIC

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks	
Viewing Angle	Horizontal	$\theta_{x+}$	60	70	--	deg	Note 1,4	
		$\theta_{x-}$	60	70	--			
	Vertical	$\theta_{y+}$	50	60	--			
		$\theta_{y-}$	30	40	--			
Contrast Ratio	CR	at optimized viewing angle	200	300	--		Note 1,3	
Response time	Rise	Tr	--	15		ms	Note 1,6	
	Fall	Tf	--	35		ms		
Uniformity		B-uni	$\theta_{x=\theta_{y=0^{\circ}}}$	70	80	--	%	Note1,5
Brightness		L	$\theta_{x=\theta_{y=0^{\circ}}}$ ADJ=3.3V	--	765	--	cd/m <sup>2</sup>	Note 1,2
Chromaticity	White	$x_W$	Center $\theta_{x=\theta_{y=0^{\circ}}}$	Typ- 0.05	Typ+ 0.05		Note 1,7	
		$y_W$						
	Red	$x_R$						
		$y_R$						
	Green	$x_G$						
		$y_G$						
	Blue	$x_B$						
		$y_B$						
Image sticking	tis	2 hours			2	Sec	Note 8	

The following optical specifications shall be measured in a darkroom or equivalent state (ambient luminance  $\leq 1$  lux, and at room temperature). The operation temperature is  $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ . The measurement method is shown in Note1.

Note1: The method of optical measurement:

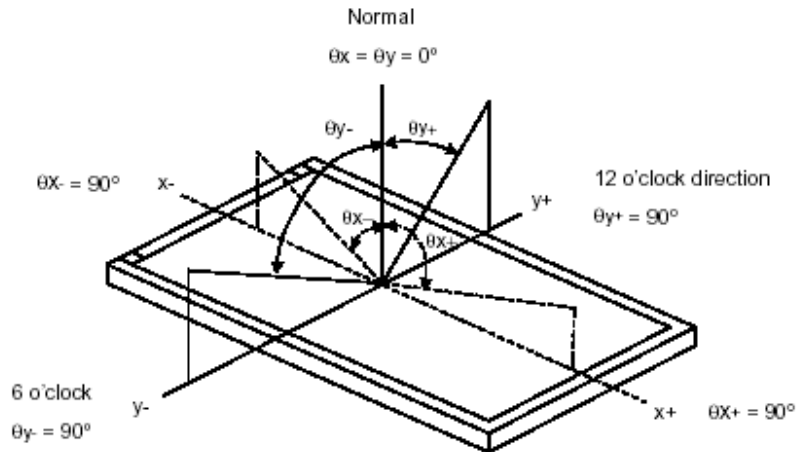


Note2: Measured at the center area of the panel and at the viewing angle of the  $\theta_x = \theta_y = 0^\circ$

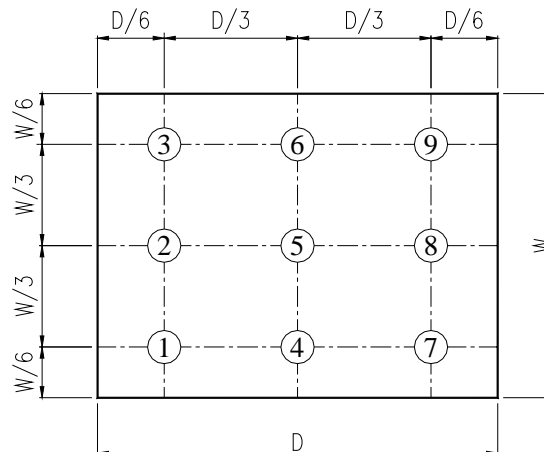
Note3: Definition of Contrast Ratio (CR):

$$CR = \frac{\text{Luminance with all pixels in white state}}{\text{Luminance with all pixels in Black state}}$$

Note4: Definition of Viewing Angle



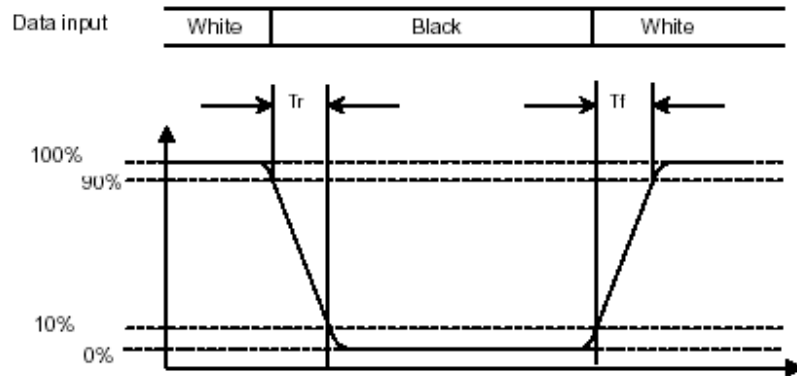
Note 5: Definition of Brightness Uniformity (B-uni):



$$B\text{-uni} = \frac{\text{Minimum luminance of 9 points}}{\text{Maximum luminance of 9 points}} \quad (\text{Note 5}).$$

Note6: Definition of Response Time:

The Response Time is set initially by defining the "Rising Time ( $T_r$ )" and the "Falling Time ( $T_f$ )" respectively.  $T_r$  and  $T_f$  are defined as following figure.



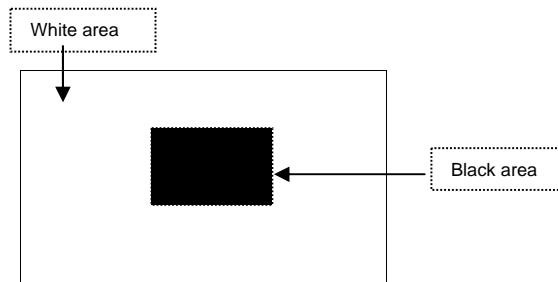
Note 7: Definition of Chromaticity:

The color coordinates  $(x_w, y_w)$ ,  $(x_r, y_r)$ ,  $(x_g, y_g)$ , and  $(x_b, y_b)$  are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.

Note 8: Definition of Image sticking ( $t_{is}$ ):

Continuously display the test pattern shown in the figure below for 2 hours. Then display a completely white screen. The previous image shall not persist more than 2 sec at 25 °C

**Image sticking pattern**



## 9. PIN CONNECTIONS

Pin NO.	SYMBOL	DESCRIPTION
1	U/D	Up or Down Display Control
2	NC	No Connection
3	HSYNC	Horizontal SYNC.
4	VLED	Power Supply for LED Driver circuit
5	VLED	Power Supply for LED Driver circuit
6	VLED	Power Supply for LED Driver circuit
7	V <sub>cc</sub>	Power Supply for LCD
8	VSYNC	Vertical SYNC.
9	DE	Data Enable
10	VSS	Power Ground
11	VSS	Power Ground
12	ADJ	Brightness control for LED B/L
13	B5	Blue Data 5 (MSB)
14	B4	Blue Data 4
15	B3	Blue Data 3
16	V <sub>ss</sub>	Power Ground
17	B2	Blue Data 2
18	B1	Blue Data 1
19	B0	Blue Data 0 (LSB)
20	V <sub>ss</sub>	Power Ground
21	G5	Green Data 5 (MSB)
22	G4	Green Data 4
23	G3	Green Data 3
24	V <sub>ss</sub>	Power Ground
25	G2	Green Data 2
26	G1	Green Data 1
27	G0	Green Data 0 (LSB)
28	V <sub>ss</sub>	Power Ground
29	R5	Red Data 5 (MSB)
30	R4	Red Data 4
31	R3	Red Data 3
32	V <sub>ss</sub>	Power Ground
33	R2	Red Data 2
34	R1	Red Data 1
35	R0	Red Data 0
36	VSS	Power Ground
37	VSS	Power Ground
38	DCLK	Clock Signals ; Latch Data at the Falling Edge
39	V <sub>ss</sub>	Power Ground
40	L/R	Left or Right Display Control

Remarks :

- 1) ADJ is brightness control Pin. The larger of the pulse duty is, the higher of the brightness.
- 2) ADJ signal is 0~3.3V. Operation frequency is 20KHz
- 3) VSS PIN must be grounding, can not be floating.

## 4) U/D and L/R control Function

L/R	U/D	Function
1	0	Normally display
0	0	Left and Right opposite
1	1	Up and Down opposite
0	1	Left and Right opposite , Up and Down opposite

5) If DE signal is fixed low, SYNC mode is used. Otherwise, DE mode is used.

**9.1 Power Signal Sequence**

Remarks:

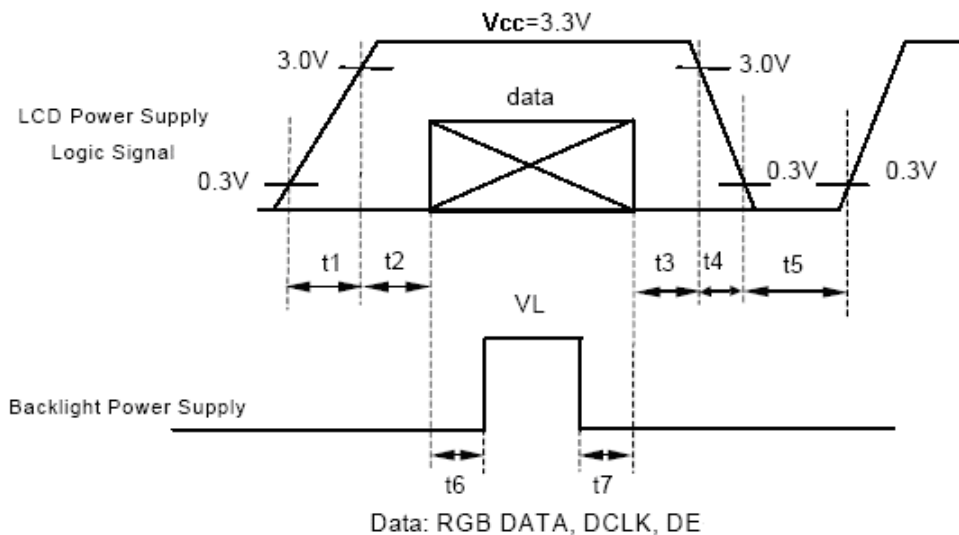
\*1) Power Signal sequence:

$t1 \leq 10\text{ms}$  :  $1 \text{ sec} \leq t5$

$50\text{ms} \leq t2$  :  $200\text{ms} \leq t6$

$0 < t3 \leq 50\text{ms}$  :  $200\text{ms} \leq t7$

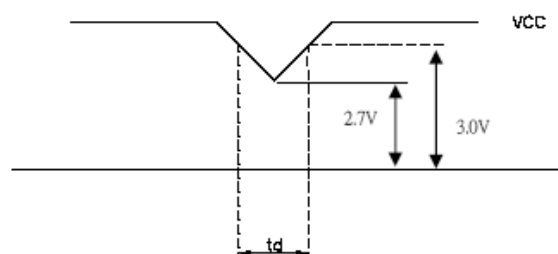
$0 < t4 \leq 10\text{ms}$



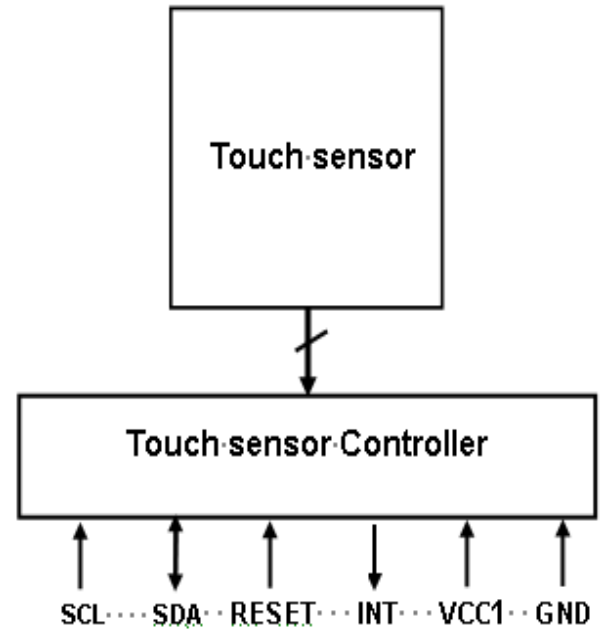
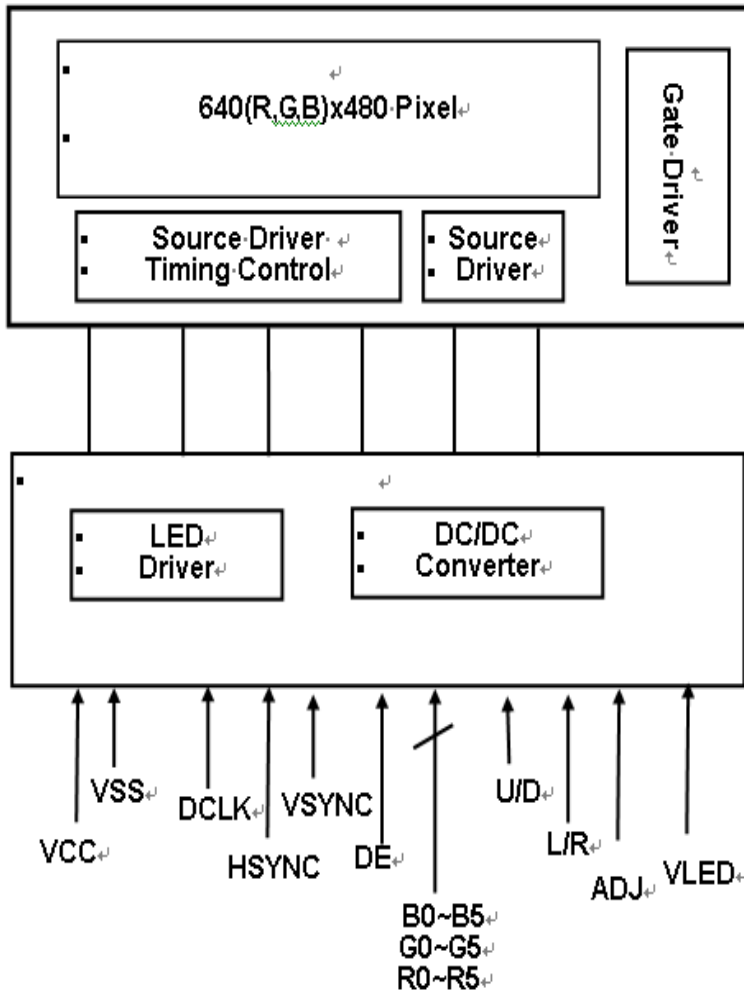
\*2) VCC-dip condition:

(1)  $2.7\text{V} \leq V_{CC} < 3.0\text{V}$ ,  $t_d \leq 10\text{ms}$

(2)  $V_{CC} > 3.0\text{V}$ , VCC-dip condition should be the same with VCC-turn-on condition.



### 10. BLOCK DIAGRAM



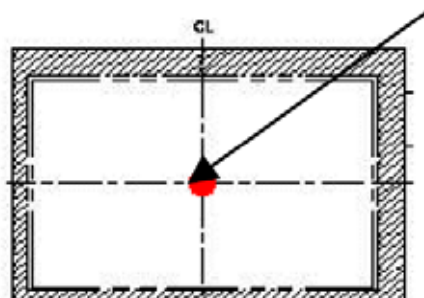
## 11. CTP General specifications

### 11.1 CTP main feature

Item	Specification	Unit
Type	Transparent type projected capacitive touch panel	
Input mode	Human's finger	
Finger	10	
Sensor Active Area	118.76(W)(typ.) x89.95(H)(typ.)	mm
Transparency	85%	%
Haze	2.0%	%
Origin Point	The upper left corner	
Hardness	7H (typ.) [by JIS K5400]	Pencil hardness
Report rate	Max : 122	Points/sec
Response time	15	ms
Point hitting life time	1,000,000 times min.	Note 1

Note 1: Use 8 mm diameter silicon rubber/force 3N to knock on the same point twice per second (no-operating), after test function check pass.

central point



### 11.2 CTP Absolute Maximum Rating

Symbol	Description	Min	Typ.	Max	Unit	Notes
VCC1	Supply voltage	2.66	-	3.47	V	
VIO	DC input voltage	0	-	VCC1+0.3	V	

### 11.3 CTP Electrical Characteristic

Symbol	Description	Min	Typ	Max	Unit	Notes
VCC1	Supply voltage	2.8	-	3.3	V	
GND	Supply voltage	-	0	-	V	
I	Active mode	-	13	15	mA	
VIH	Input H voltage	1.35	1.8	2.1	V	
VIL	Input L voltage	-0.3	0	0.45	V	



### 11.4 CTP Pin Connections

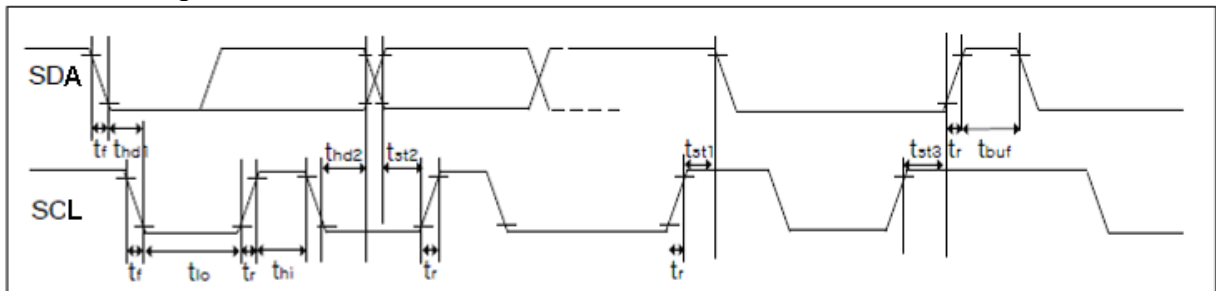
No.	Name	I/O	Description
1	NC	-	No connection
2	SCL	I	I <sup>2</sup> C Clock
3	SDA	I/O	I <sup>2</sup> C Data
4	NC	-	No connection
5	INT	O	Interrupt output
6	GND	P	Ground
7	VCC1	P	Power supply Voltage
8	/RESET	I	Reset active low
9	NC	-	No connection

### 11.5 CTP Interface and Data Format [Slave address is 0x5D ( 7 bit addressing ) ]

Communication protocol: I<sup>2</sup>C

Clock frequency : 100Khz ( 400Khz Fast mode )

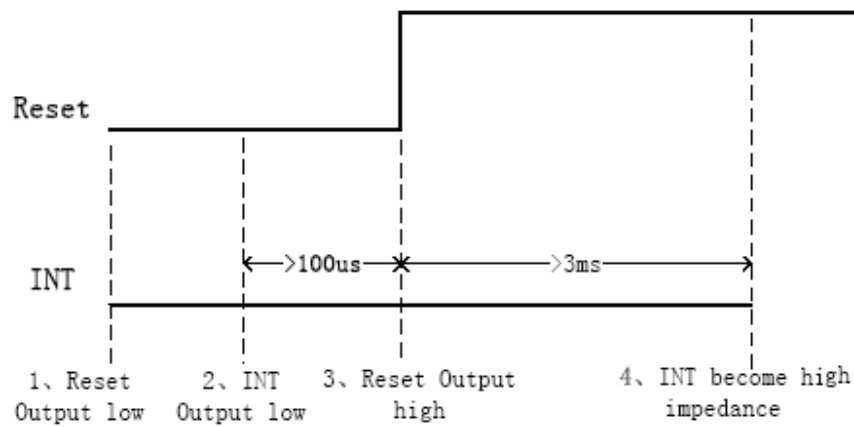
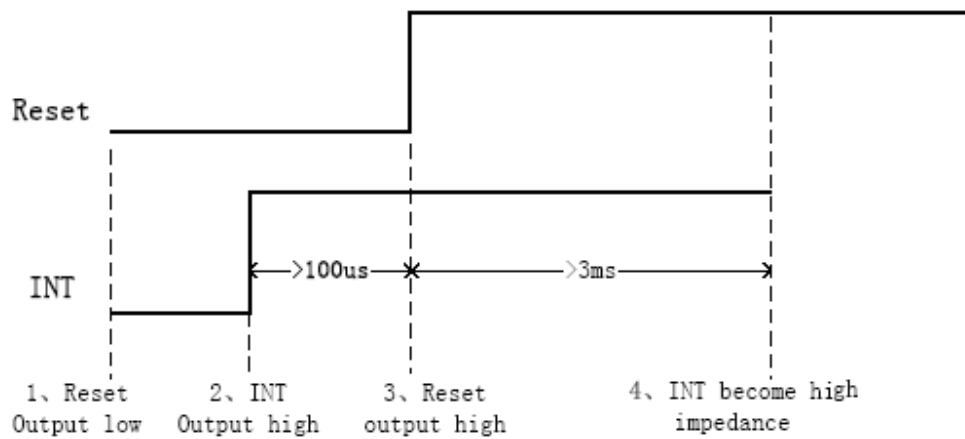
Below is timing of I2C hardware circuit:



Test condition 1: 3.3V communication interface, 400Kbps, pull up resistor is 2K ohm

Parameter	Symbol	Min	Max	Unit
SCL low period	$t_{lo}$	0.9	0.9	us
SCL high period	$t_{hi}$	0.8	0.8	us
SCL setup time for START condition	$t_{st1}$	0.4	0.4	us
SCL setup time for STOP condition	$t_{st3}$	0.4	0.4	us
SCL hold time for START condition	$t_{st1}$	0.3	0.3	us
SDA setup time	$t_{st2}$	0.4	0.4	us
SDA hold time	$t_{st2}$	0.4	0.4	us

GT927 has 2 sets of slave address 0xBA/0xBB or 0x28/0x29. Master can control Reset & INT pin to configure the slave address the slave address in power on initial state like following

**11.5.1 Timing of setting slave address to 0xBA/0XB8:**

**11.5.2 Timing of setting slave address to 0x28/0X29:**


**a) Data Transmission** (ex: slave address is 0xBA/0xBB)

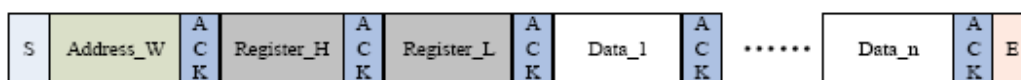
Communication is always initiated by master, A high-to-low transition of SDA with SCL high is a start condition.

All addresses words are serially transmitted to and from on bus in 8-bit words. GT927 sends a “0” to acknowledge when the address word is 0xBA/BB. This happens during the ninth clock cycle. If the slave address is not matched, GT927 will stay in idle state.

The data words are serially transmitted to and from in 9-bit words: 8-bit data + 1-bit ACK or NACK sent by GT927. Data changes during SCL high periods.

A low-to-high transition of SDA with SCL high is a stop condition.

**b) Write Operations to GT927** (ex: slave address is 0xBA/0xBB)



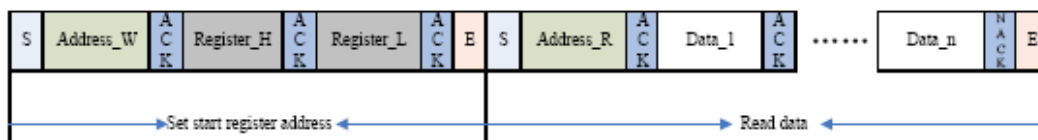
**Write Operations**

Please check above figure, master start the communication first, and then sends address words 0xBA for a write operation.

After receiving ACK from GT927, master sends out register address word in 16-bit, and then the data word in 8-bit, which is going to be wrote into GT927.

GT927’s address pointer will be automatically added 1 after write operation, so master can sequential write in one operation. When operation finished, master stop the communication.

**c) Read Operations to GT927** (ex: slave address is 0xBA/0xBB)



**Read operation**

Please check above figure, master start the communication first, and then sends address words 0xBA for a write operation.

After receiving ACK from GT927, master sends out register address word in 16-bit, to set GT927’s address pointer. After receiving ACK, master sends out a start signal once again, start the read operation with command: 0xBB, and read data word from GT927 in 8-bit.

GT927 also supports sequential read operation, and the default setting is sequential read mode. Master shall send out ACK when receiving successfully in every data word, master sends NACK after getting all the data required, then sends stop signal to finish the communication.

### 11.5.3 Register Information of GT927

#### a) Real Time Order (Write Only)

Addr	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0x8040	Command	0: read status of coordinate 1:D-value 2: software reset 3: benchmark update 4: benchmark calibration 5: screen off							
0x8041	LED_Control	Control word under control of touch key LED light							
0x8042	Proximity_En	Proximity switch							

#### b) Configuration Information (R/W)

Addr	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0x8047	Config_Version	Version number of configuration document							
0x8048	X Output Max_L	Max value of X axis							
0x8049	X Output Max_H								
0x804A	Y Output Max_L	Max value of Y axis							
0x804B	Y Output Max_H								
0x804C	Touch Number	Reserved				Touch number: 1~5			
0x804D	Module_Switch1	Reserved	Stretch_rank		X2Y	Sito	INT trigger method		
0x804E	Module_Switch2	Reserved				Touch_Key			
0x804F	Shake_Count	Reserved				Finger shake count			
0x8050	Filter	First_Filter		Normal_Filter(filtering value of original coordinate window, coefficient is 1)					
0x8051	Large_Touch	Number of touch in large area							
0x8052	Noise_Reduction	Reserved			Value of noise elimination(coefficient is 1)				
0x8053	S_Touch_Level	Threshold of touch grow out of nothing							
0x8054	S_Leave_Level	Threshold of touch grow out of nothing							
0x8055	Low_Power_Control	Reserved				Time to low power consumption(0~15s)			
0x8056	Refresh_Rate	Reserved				Coordinate report rate(Cycle: 5+N ms)			
0x8057	x_threshold	Reserved							
0x8058	y_threshold								
0x8059	X_Speed_Limit	Parameter of speed limit							
0x805A	Y_Speed_Limit								
0x805B	Space	Blank area of boarder-top			Blank area of boarder-bottom				
0x805C		Blank area of boarder-left			Blank area of boarder-right				
0x805D	NC	Reserved							
0x805E	NC	Reserved							
0x805F	NC	Reserved							
0x8060	NC	Reserved							
0x8061	NC	Reserved							
0x8062	Drv_GroupA_Num	All_Driving	Reserved			Driver_Group_A_number			
0x8063	Drv_GroupB_Num	Reserved		D_Freq		Driver_Group_B_number			
0x8064	Sensor_Num	Sensor_Group_B_Number			Sensor_Group_A_Number				
0x8065	FreqA_factor	Driver frequency double frequency coefficient of Driver group A GroupA_Frequence =Multiplier factor *baseband							
0x8066	FreqB_factor	Driver frequency double frequency coefficient of Driver group B GroupB_Frequence =Multiplier factor *baseband							
0x8067	Pannel_BitFreqL	Baseband of Driver group A\B(1526Hz<baseband<14600Hz)							
0x8068	Pannel_BitFreqH								
0x8069	Pannel_Sensor_TimeL	Time interval of the neighbouring two driving signal(Unit: us)							
0x806A	Pannel_Sensor_TimeH								

0x806B	Pannel_Tx_Gain	Reserved	Pannel_Drv_output_R, 4 gears	Pannel_DAC_Gain
0x806C	Pannel_Rx_Gain	Pannel_PGA _C	Pannel_PGA_R	Pannel_Rx _Vcmi
0x806D	Pannel_Dump_Shift	Reserved		Magnification coefficient of original value(The Nth power of 2)
0x806E	Drv_Frame_Control	Reserved		Repeat_Num
0x806F	NC	Reserved		
0x8070	NC	Reserved		
0x8071	NC	Reserved		
0x8072	Stylus_Tx_Gain	Undefined(Invalid when stylus_priority=0)		
0x8073	Stylus_Rx_Gain	Undefined(Invalid when stylus_priority=0)		
0x8074	Stylus_Dump_Shift	Undefined(Invalid when stylus_priority=0)		
0x8075	Stylus_Touch_Level	Undefined(Invalid when stylus_priority=0)		
0x8076	Stylus_Leave_Level	Undefined(Invalid when stylus_priority=0)		
0x8077	Stylus_Control	Pen mode escape time out period(Unit: Sec)		
0x8078	NC	Reserved		
0x8079	NC	Reserved		
0x807A	Freq_Hopping_Start	Frequency hopping start frequency(Unit: 2KHz,50means 100KHz)		
0x807B	Freq_Hopping_End	Frequency hopping stop frequency(Unit: 2KHz,150means 300KHz)		
0x807C	Noise_Detect_Tims	Detect_Stay_Times	Detect_Confirm_Times	
0x807D	Hopping_Flag	Hop_En	Reserved	Detect_Time_Out
0x807E	Hopping_Threshold	Large_Noise_Threshold		Large_Hit_Threshold
0x807F	Noise_Threshold	Threshold of noise level		
0x8080	NC	Reserved		
0x8081	NC	Reserved		
0x8082	Hopping_seg1_BitFreqL	Frequency hopping segment band 1 central frequency(for driver A/B)		
0x8083	Hopping_seg1_BitFreqH			
0x8084	Hopping_seg1_Factor	Frequency hopping segment 1 central frequency coefficient		
0x8085	Hopping_seg2_BitFreqL	Frequency hopping segment band 2 central frequency(for driver A/B)		
0x8086	Hopping_seg2_BitFreqH			
0x8087	Hopping_seg2_Factor	Frequency hopping segment 2 central frequency coefficient		
0x8088	Hopping_seg3_BitFreqL	Frequency hopping segment band 3 central frequency(for driver A/B)		
0x8089	Hopping_seg3_BitFreqH			
0x808A	Hopping_seg3_Factor	Frequency hopping segment 3 central frequency coefficient		
0x808B	Hopping_seg4_BitFreqL	Frequency hopping segment band 4 central frequency(for driver A/B)		
0x808C	Hopping_seg4_BitFreqH			
0x808D	Hopping_seg4_Factor	Frequency hopping segment 4 central frequency coefficient		
0x808E	Hopping_seg5_BitFreqL	Frequency hopping segment band 5 central frequency(for driver A/B)		
0x808F	Hopping_seg5_BitFreqH			
0x8090	Hopping_seg5_Factor	Frequency hopping segment 5 central frequency coefficient		
0x8091	NC	Reserved		
0x8092	NC	Reserved		
0x8093	Key1	Key 1 Position: 0~255 valid(0 means no touch, it means independent touch key when 4 of the keys are 8 multiples)		
0x8094	Key2	Key 2 position		
0x8095	Key3	Key 3 position		
0x8096	Key4	Key 4 position		
0x8097	Key_Area	Time limit for long press(1~16s)		Touch valid interval setting: 0~15 valid
0x8098	Key_Touch_Level	Key threshold of touch key		

0x8099	Key_Leave_Level	Key threshold of touch key	
0x809A	Key_Sens	KeySens_1(sensitivity coefficient of key 1,same below)	KeySens_2
0x809B	Key_Sens	KeySens_3	KeySens_4
0x809C	Key_Restrain	Reserved	Reserved
0x809D	NC	Reserved	
0x809E	NC	Reserved	
0x809F	NC	Reserved	
0x80A0	NC	Reserved	
0x80A1	NC	Reserved	
0x80A2	Proximity_Drv_Select	Drv_Start_Ch(start channel of driving direction)	Drv_End_Ch(End channel)
0x80A3	Proximity_Sens_Select	Sens_Start_Ch(start channel of sensing direction)	Sens_End_Ch(End channel)
0x80A4	Proximity_Touch_Level	Proximity effective threshold value	
0x80A5	Proximity_Leave_Level	Proximity ineffective threshold value	
0x80A6	Proximity_Freq_Factor	Frequency mollification of proximity sensing channel	
0x80A7	Proximity_BitFreqL	Base frequency of proximity sensing channel	
0x80A8	Proximity_BitFreqH		
0x80A9	Proximity_Sensor_TimeL	Time interval between proximity adjacent driving signal	
0x80AA	Proximity_Sensor_TimeH		
0x80AB	Proximity_Tx_Gain	Driving gain of proximity	
0x80AC	Proximity_Rx_Gain	Driving gain of proximity	
0x80AD	Proximity_Dump_Shift	Reserved	Magnification coefficient of proximity original value(The Nth power of 2)
0x80AE	NC	Reserved	
0x80AF	NC	Reserved	
0x80B0	NC	Reserved	
0x80B1	NC	Reserved	
0x80B2	NC	Reserved	
0x80B3	NC	Reserved	
0x80B4	NC	Reserved	
0x80B5	NC	Reserved	
0x80B6	NC	Reserved	
0x80B7~ 0x80C4	Sensor_CH0~ Sensor_CH13	Corresponding channel no. of ITO Sensor	
0x80C5~ 0x80D4	NC	Reserved	
0x80D5~ 0x80EA	Driver_CH1~ Driver_CH21	Corresponding channel no. of ITO Driver0	
0x80EB~ 0x80FE	NC	Reserved	
0x80FF	Config_Chksum	Check of configuration information	
0x8100	Config_Fresh	Updated configuration(by master control)	

**c) Coordinates Information**

Addr	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
0x8140	Product ID(Lowest Byte, ASCII code 6)							
0x8141	Product ID(Third Byte, ASCII code 0)							
0x8142	Product ID(Second Byte, ASCII code 0)							
0x8143	Product ID(Highest Byte, ASCII code 9)							
0x8144	Firmware version(byte1)(Low Byte)							
0x8145	Firmware version(byte2)(High Byte)							
0x8146	x coordinate resolution(low byte)(current output resolution)							
0x8147	x coordinate resolution(high byte)							
0x8148	y coordinate resolution(low byte)							
0x8149	y coordinate resolution(high byte)							
0x814A	Vendor_id(current module choice information)							
0x814B	Reserved							
0x814C	gesture type(Reserved)							
0x814D	gesture value(Reserved)							
0x814E	buffer status	Large detect	Proximity Valid		HaveKey		number of touch points	
0x814F	track id							
0x8150	point 1 x coordinate(low byte)							
0x8151	point 1 x coordinate(high byte)							
0x8152	point 1 y coordinate(low byte)							
0x8153	point 1 y coordinate(high byte)							
0x8154	point 1 size(low byte)							
0x8155	point 1 size(high byte)							
0x8156	Reserved							
0x8157	track id							
0x8158	point 2 x coordinate(low byte)							
0x8159	point 2 x coordinate(high byte)							
0x815A	point 2 y coordinate(low byte)							
0x815B	point 2 y coordinate(high byte)							
0x815C	point 2 size(low byte)							
0x815D	point 2 size(high byte)							
0x815E	Reserved							
0x815F	track id							
0x8160	point 3 x coordinate(low byte)							
0x8161	point 3 x coordinate(high byte)							
0x8162	point 3 y coordinate(low byte)							
0x8163	point 3 y coordinate(high byte)							
0x8164	point 3 size(low byte)							
0x8165	point 3 size(high byte)							
0x8166	Reserved							
0x8167	track id							
0x8168	point 4 x coordinate(low byte)							
0x8169	point 4 x coordinate(high byte)							
0x816A	point 4 y coordinate(low byte)							
0x816B	point 4 y coordinate(high byte)							
0x816C	point 4 size(low byte)							
0x816D	point 4 size(high byte)							
0x816E	Reserved							
0x816F	track id							
0x8170	point 5 x coordinate(low byte)							
0x8171	point 5 x coordinate(high byte)							
0x8172	point 5 y coordinate(low byte)							

0x8173	point 5 y coordinate(high byte)
0x8174	point 5 size(low byte)
0x8175	point 5 size(high byte)
0x8176	Reserved
0x8177	track id
0x8178	point 6 x coordinate(low byte)
0x8179	point 6 x coordinate(high byte)
0x817A	point 6 y coordinate(low byte)
0x817B	point 6 y coordinate(high byte)
0x817C	point 6 size(low byte)
0x817D	point 6 size(high byte)
0x817E	Reserved
0x817F	track id
0x8180	point 7 x coordinate(low byte)
0x8181	point 7 x coordinate(high byte)
0x8182	point 7 y coordinate(low byte)
0x8183	point 7 y coordinate(high byte)
0x8184	point 7 size(low byte)
0x8185	point 7 size(high byte)
0x8186	Reserved
0x8187	track id
0x8188	point 8 x coordinate(low byte)
0x8189	point 8 x coordinate(high byte)
0x818A	point 8 y coordinate(low byte)
0x818B	point 8 y coordinate(high byte)
0x818C	point 8 size(low byte)
0x818D	point 8 size(high byte)
0x818E	Reserved
0x818F	track id
0x8190	point 9 x coordinate(low byte)
0x8191	point 9 x coordinate(high byte)
0x8192	point 9 y coordinate(low byte)
0x8193	point 9 y coordinate(high byte)
0x8194	point 9 size(low byte)
0x8195	point 9 size(high byte)
0x8196	Reserved
0x8197	track id
0x8198	point 10 x coordinate(low byte)
0x8199	point 10 x coordinate(high byte)
0x819A	point 10 y coordinate(low byte)
0x819B	point 10 y coordinate(high byte)
0x819C	point 10 size(low byte)
0x819D	point 10 size(high byte)
0x819E	Reserved
0x819F	Keyvaule



## 12. Appearance Specification

### 12.1 Inspection condition

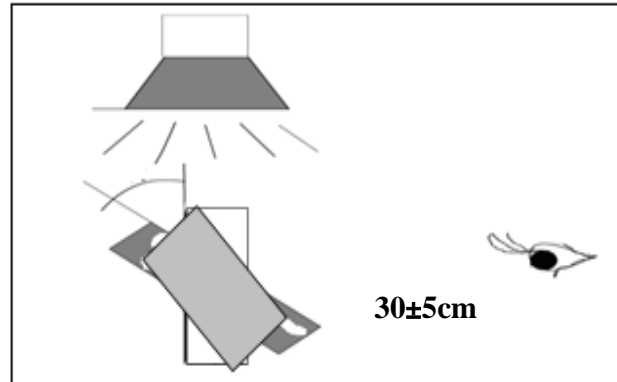
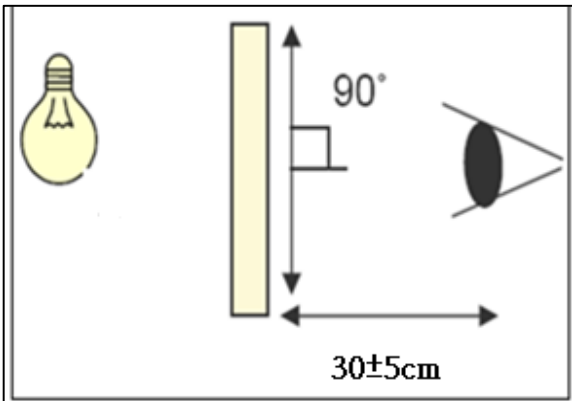
#### 12.1.1 Inspection conditions

12.1.1.1 Inspection Distance :  $30 \pm 5$  cm

12.1.1.2 View Angle :

(1) Inspection that light pervious to the product:  $90 \pm 15^\circ$

(2) Inspection that light reflects on the product:  $90 \pm 15^\circ$

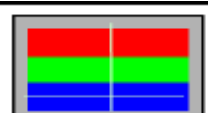


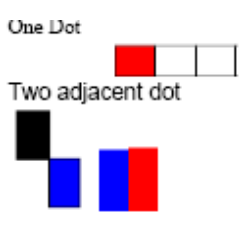
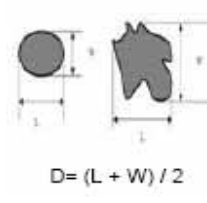
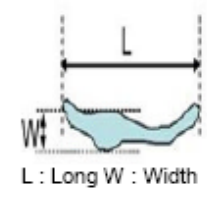
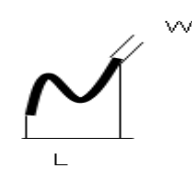
#### 12.1.2 Environment conditions :

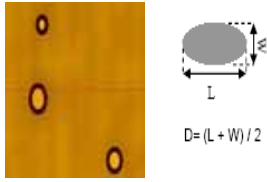
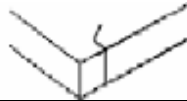
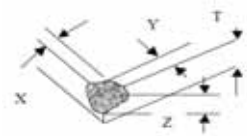

Ambient Temperature :	$25 \pm 5$
Ambient Humidity :	30~75%RH
Ambient Illumination	600~800 lux

### 12.2 Inspection Parameters

Appearance inspection standard (D: diameter, L: length; W: width, Z: height, T: glass thickness)

Inspection item	Inspection standard	Description
No image	Prohibited	
Image abnormal	Prohibited	
Bright line	Prohibited	
Thin line	It is acceptable that the defect can not be seen with 10% ND filter.	
Mura	It is acceptable that the defect can not be seen with 5% ND filter.	

Dot	Item	Acceptable Visible area	Total	
	Bright dot	2		
	Dark dot	4		
	Bright adjacent dots	1	1	
	Dark adjacent dots	2	2	
	Adjacent dots with a bright dot and a dark dot	1	1	
Foreign material in dot shape	SPEC (unit: mm)		Acceptable	 <p><math>D = (L + W) / 2</math></p>
	D ≤ 0.3		Ignored	
	0.3 < D ≤ 0.5, distance > 5		n 5	
	D > 0.5		0	
Inspection item	Inspection standard		Description	
Foreign material in line shape	SPEC		Acceptable	 <p>L : Long W : Width</p>
	W ≤ 0.05 and L ≤ 7		Ignored	
	0.05 < W ≤ 0.1, L ≤ 7, distance > 5		n 5	
	W > 0.1 or L > 7		0	
Contamination	It is acceptable if the dirt can be wiped.			
Inspection item	SPEC		Description	
Scratch	SPEC		Acceptable	
	W ≤ 0.05 and L ≤ 7		Ignored	
	0.05 < W ≤ 0.08, L ≤ 7, distance > 5		n 5	
	0.08 < W ≤ 0.1, L ≤ 7, distance > 5		n 3	
	W > 0.1 or L > 7		0	

Bubble	SPEC (unit: mm)	Acceptable	
	D 0.2	Ignored	
	Non visible area	Ignored	
	0.2 < D 0.3, distance > 5	n 5	
	D > 0.3	0	
Cover & Sensor Crack	Prohibited		
Cover angle missing	SPEC (unit: mm)	Acceptable	
	Side/Bottom	Ignored	
	It is prohibited if the defect appears on the front.	0	
Cover edge break	SPEC (unit: mm)	Acceptable	
	X 2.0, Y 2.0, Z 1/2T	Ignored	
	X > 2.0, Y > 2.0, Z > 1/2T	0	
Inspection item	SPEC		Description
Ink	SPEC (unit: mm)	Acceptable	
	word unclear, inverted, mistake, break line	0	
Bubble under protection film	SPEC (unit: mm)	Acceptable	
	NA		
Function	Prohibited		

### 12.3 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling

Sampling table: MIL-STD-105E

Inspection level: Level II

Class of defects	Definition		
	Major	AQL 0.65%	It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function.
	Minor	AQL 1.5%	It is a defect that will not result in functioning problem with deviation classified.

### 13. QUALITY ASSURANCE

#### 13.1 Test Condition

##### 13.1.1 Temperature and Humidity(Ambient Temperature)

Temperature :  $25 \pm 5^{\circ}\text{C}$

Humidity :  $65 \pm 5\%$

##### 13.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

##### 13.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

##### 13.1.4 Test Frequency

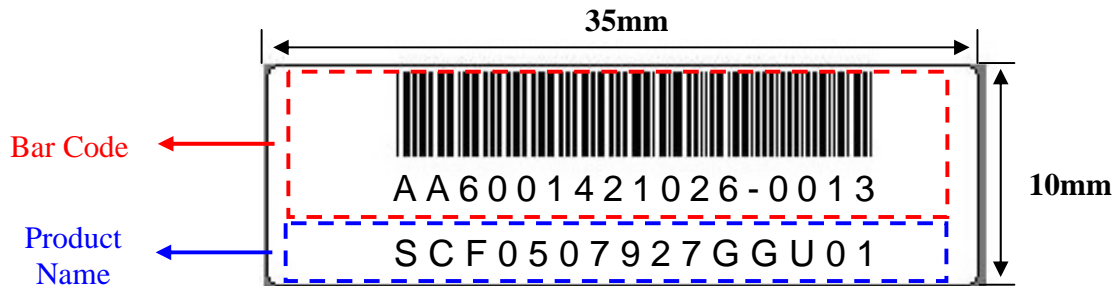
In case of related to deterioration such as shock test. It will be conducted only once.

##### 13.1.5 Test Method

Reliability Test Item & Level		Test Level	Remark
No.	Test Item		
1.	Low Temperature Storage Test	T= -30 ,120hrs after 24 hrs at room temperature and test.	IEC68-2-1
2.	High Temperature Storage Test	T= 80 ,120hrs after 24 hrs at room temperature and test.	IEC68-2-2
3.	Low Temperature Operation Test	T= -20 ,120hrs after 24 hrs at room temperature and test.	IEC68-2-1
4.	High Temperature Operation Test	T= 70 ,120hrs after 24 hrs at room temperature and test.	IEC68-2-2
5.	High Temperature and High Humidity Operation Test	T= 40 , 90%RH,120hrs after 24 hrs at room temperature and test.	IEC68-2-3
6.	Thermal Cycling Test (No operation)	-20 30min ~ 70 30 min , 100 Cycles after 24 hrs at room temperature and test.	IEC68-2-14
7.	Vibration Test (No operation)	Frequency :10 ~ 55 HZ Amplitude :1.5 mm Sweep time : 11 mins Test Period: 6 Cycles for each direction of X, Y, Z	IEC68-2-6
8.	ESD TEST	Air Discharge : $\pm 15\text{KV}$ Indirect Contact Discharge : $\pm 8\text{KV}$	IEC-61000-4-2

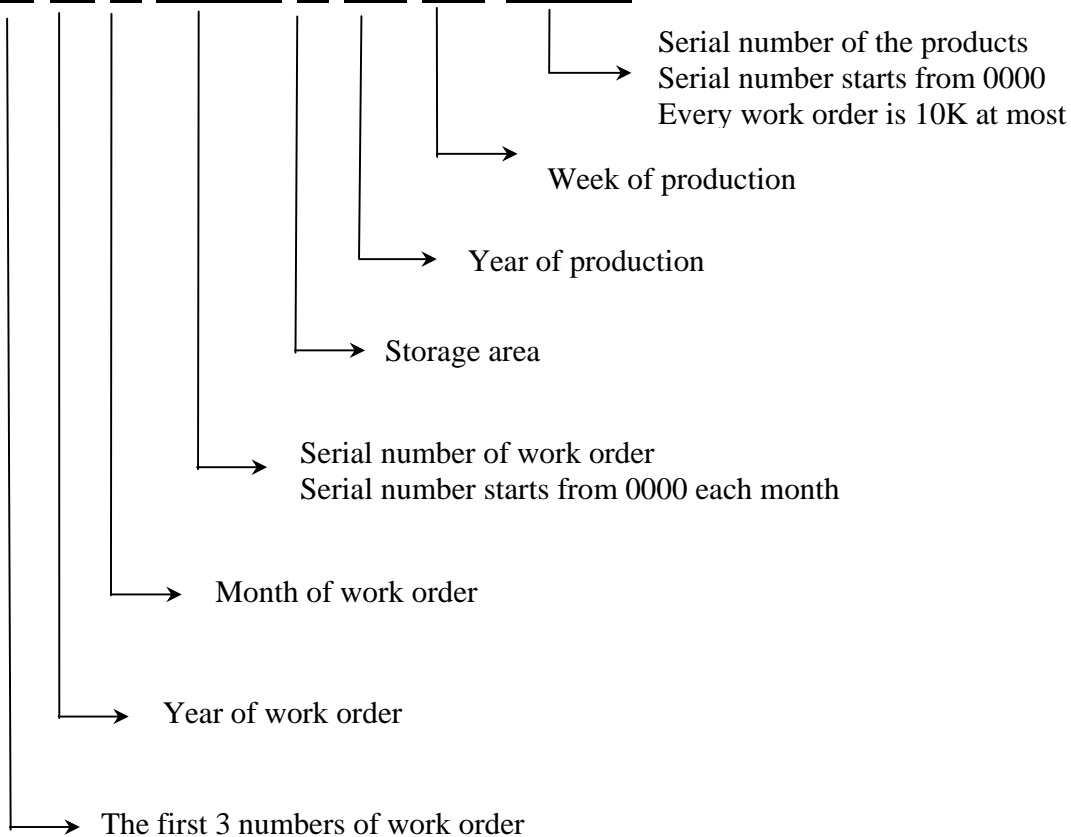
## 14. LCM PRODUCT LABEL DEFINE

Product Label style:



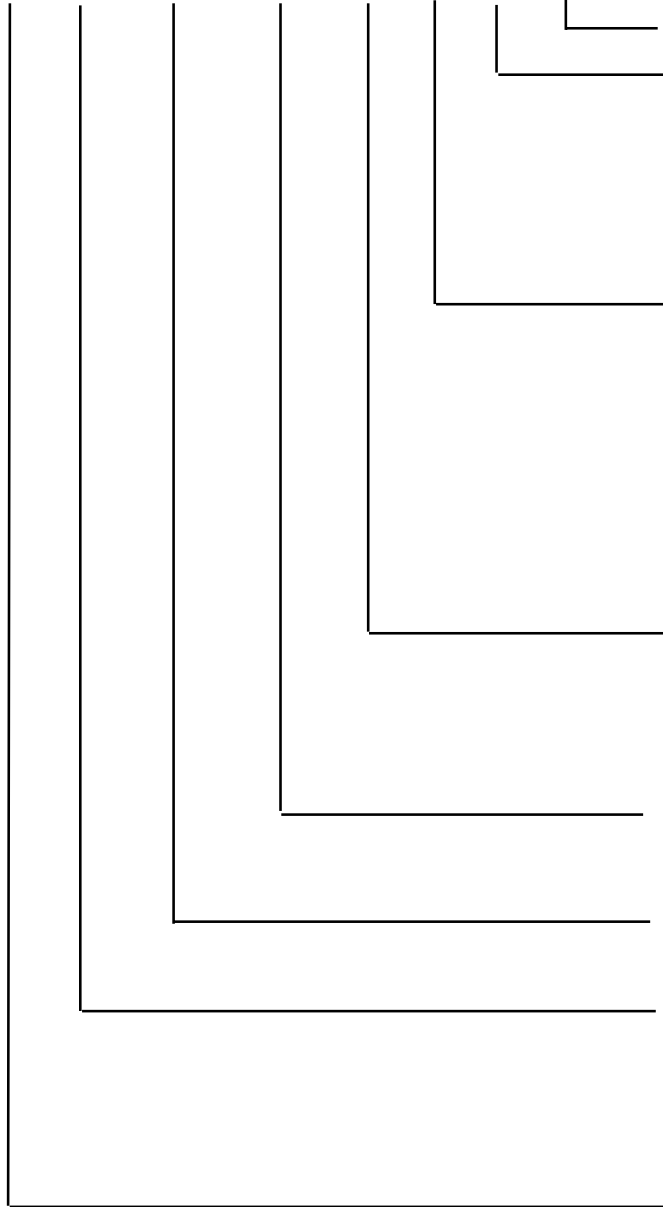
BarCode Define:

**A A 6 0014 2 10 26-0013**



**Product Name Define:**

**SC F 0507 927 G G U 01**



**Serial Number**

**Material of Glue**

N : None

R : Other

C : OCA

**U : UV**

**Material of Cover Lens**

N : None

**G : Glass**

P : PMMA

C : PC

F : Film

R : Other

**Material of Sensor**

**G : Glass**

F : Film

R : Other

**IC Number**

**927 : GT927**

**Size**

**0507 : 5.7inch**

**Module Type**

N : None LCM

**F : Standard CTP Module**

X : Custom CTP Module

**Capacitive Touch Panel**

## 15. PRECAUTIONS IN USE LCM

### 1. LIQUID CRYSTAL DISPLAY (LCD)

LCD is made up of glass, organic sealant, organic fluid, and polymer based polarizers. The following precautions should be taken when handling,

- (1). Keep the temperature within range of use and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel off or bubble.
- (2). Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzine.
- (3). Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.
- (4). Glass can be easily chipped or cracked from rough handling, especially at corners and edges.
- (5). Do not drive LCD with DC voltage.

### 2. Liquid Crystal Display Modules

#### 2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.

- (1). Do not tamper in any way with the tabs on the metal frame.
- (2). Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
- (3). Do not touch the elastomer connector, especially insert an backlight panel (for example, EL).
- (4). When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- (5). Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

#### 2.2. Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

- (1). The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- (2). The modules should be kept in antistatic bags or other containers resistant to static for storage.
- (3). Only properly grounded soldering irons should be used.
- (4). If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

- (5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.
- (6). Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

#### 2.3 Soldering

- (1). Solder only to the I/O terminals.
- (2). Use only soldering irons with proper grounding and no leakage.
- (3). Soldering temperature :  $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$
- (4). Soldering time: 3 to 4 sec.
- (5). Use eutectic solder with resin flux fill.
- (6). If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed after wards.

#### 2.4 Operation

- (1). The viewing angle can be adjusted by varying the LCD driving voltage  $V_0$ .
- (2). Driving voltage should be kept within specified range; excess voltage shortens display life.
- (3). Response time increases with decrease in temperature.
- (4). Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- (5). Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

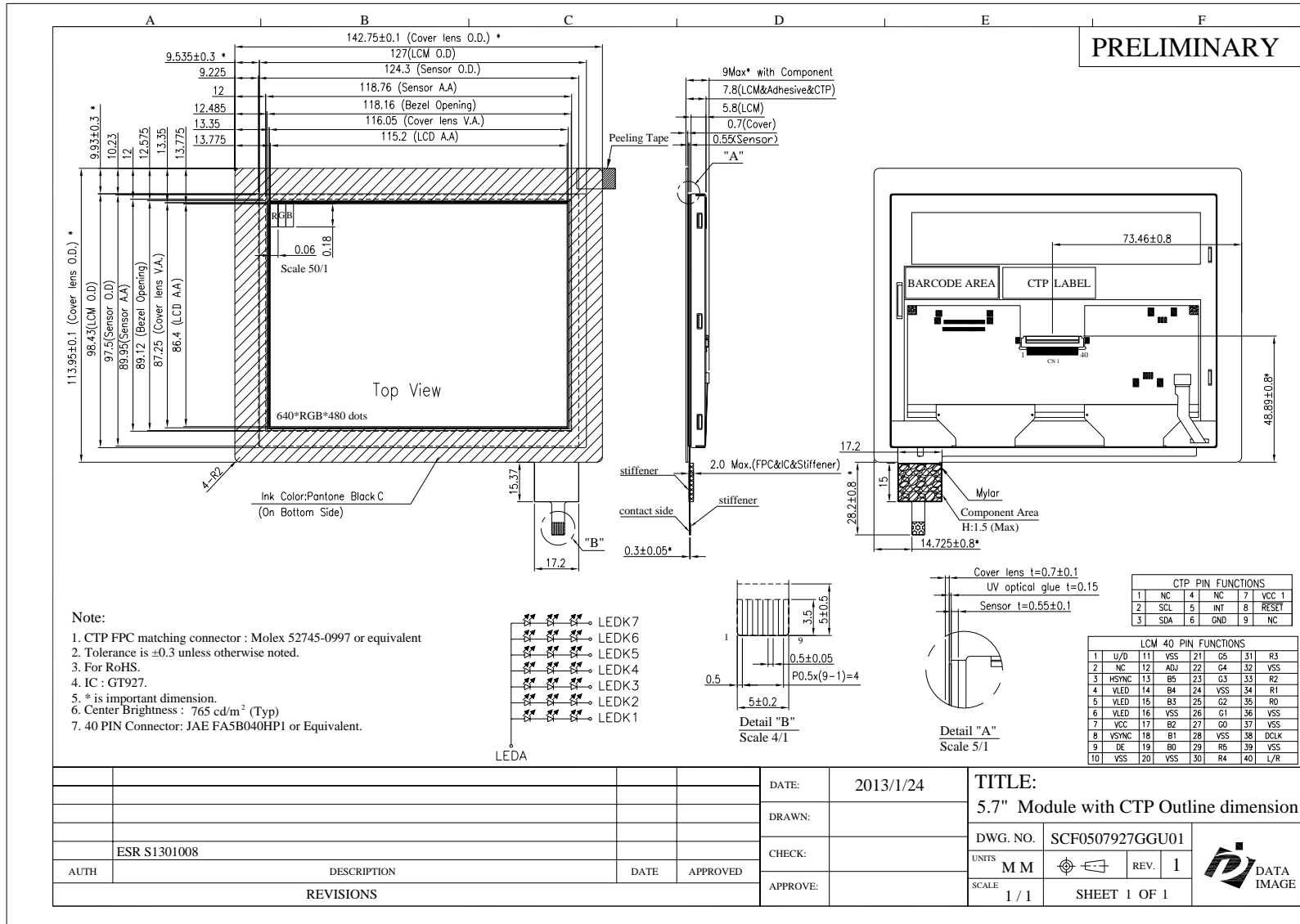
#### 2.5 Storage

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

#### 2.6 Limited Warranty

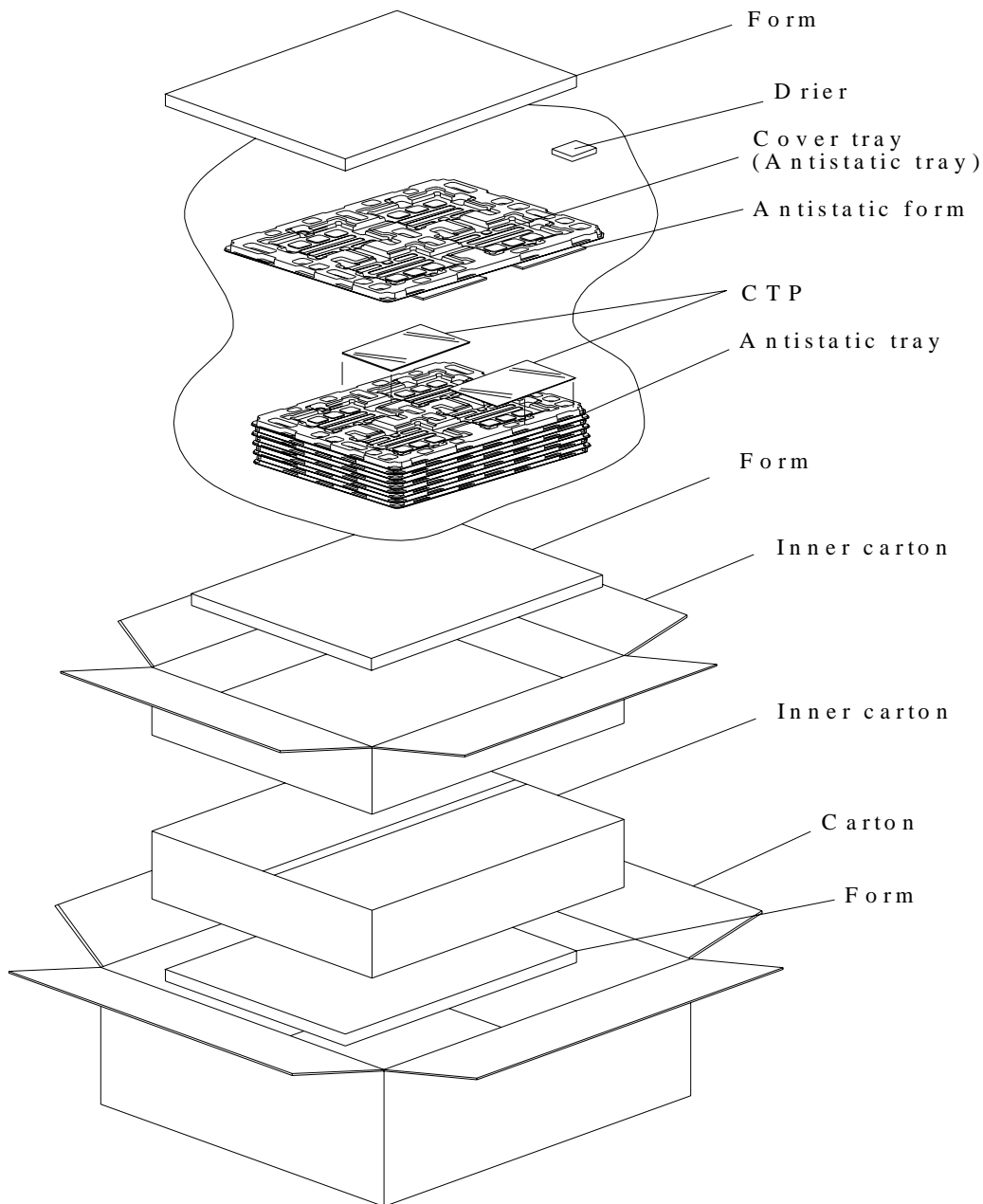
Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not be responsible for any subsequent or consequential events.

### 16. OUTLINE DRAWING





## 17. PACKAGE INFORMATION



1 Inner carton = 32 pcs

1 Carton = 2 Inner carton = 32 pcs \* 2 = 64 pcs

Carton size : 465L x 380W x 395H (mm)

Total Weight =: 13 kgw