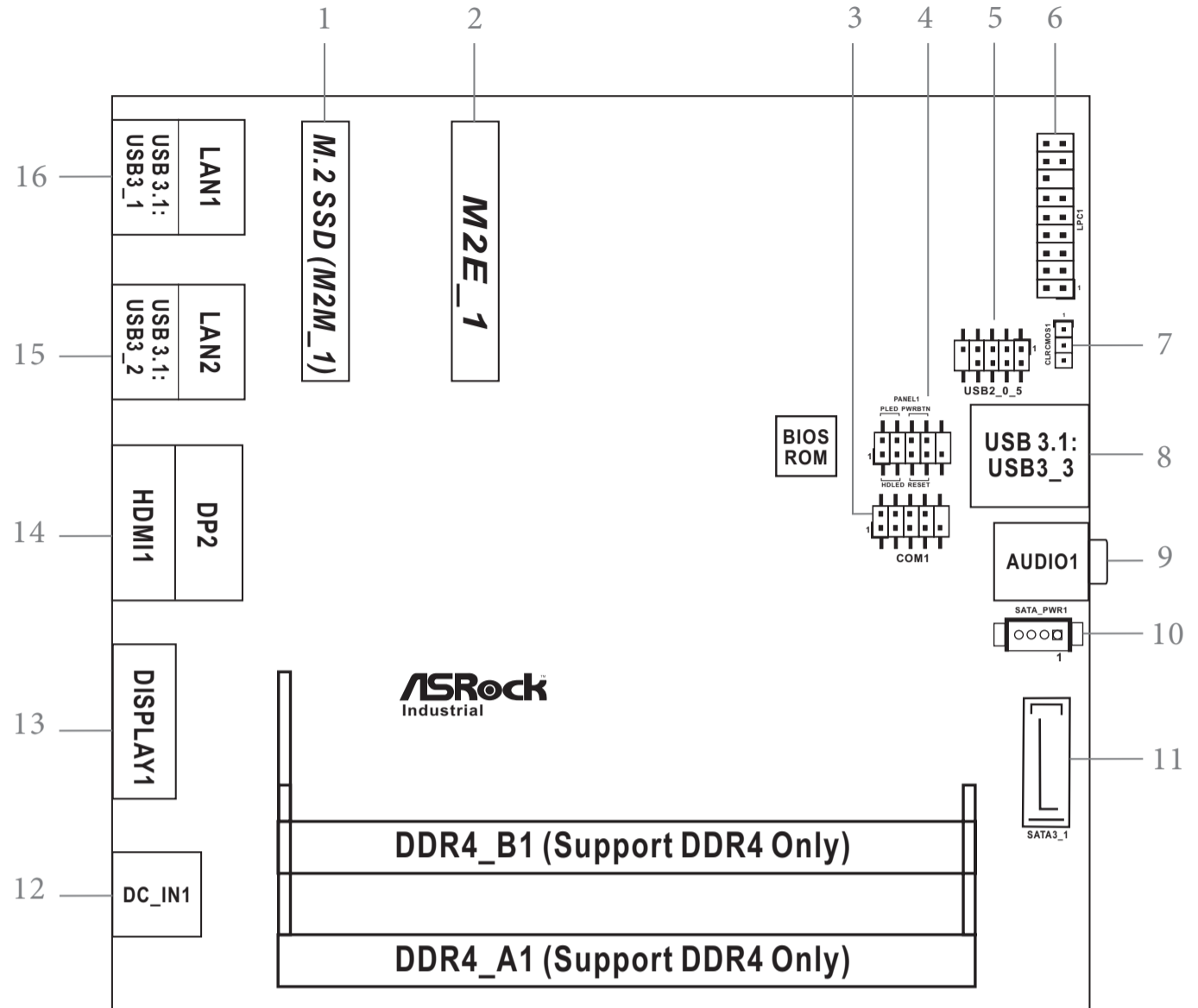


**4X4-R1000M**  
**4X4-R1000V**  
**4X4-V1000M**

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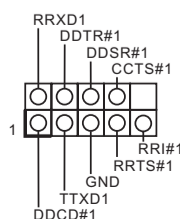
### 1 : M.2 Key-M Socket (M2M\_1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERp3	NA	8
9	GND	SATA_LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERn0/SATA-B+	SMB_DATA	42
43	PERp0/SATA-B-	NA	44
45	GND	NA	46
47	PETn0/SATA-A-	NA	48
49	PETp0/SATA-A+	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

### 2 : M.2 Key-E Socket (M2E\_1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BGI_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST#	52
55	WAKE#	W_DISABLE1#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

### 3 : COM Port Header (RS232/422/485)

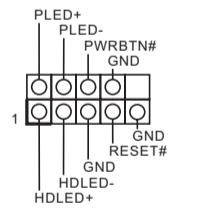


\* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

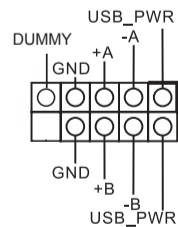
#### COM1 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD, Data Carrier Detect	TX-	RTX-
2	RXD, Receive Data	TX+	RTX+
3	TXD, Transmit Data	RX+	N/A
4	DTR, Data Terminal Ready	RX-	N/A
5	GND	GND	GND
6	DSR, Data Set Ready	N/A	N/A
7	RTS, Request To Send	N/A	N/A
8	CTS, Clear To Send	N/A	N/A
9	N/A	N/A	N/A

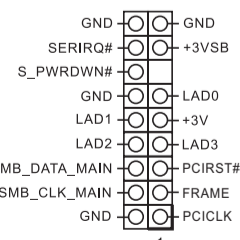
### 4 : System Panel Header (PANEL1)



### 5 : USB2.0 Connector (USB2\_0\_5)



### 6 : LPC Debug Header (LPC1)



### 7 : Clear CMOS Headers (CLRCMOS1)

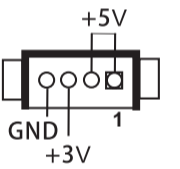
1-2 : Normal  
2-3 : Clear CMOS



### 8 : USB3.1 Port (USB3\_3)

### 9 : Audio Jack (AUDIO1)

### 10 : SATA Power Output Connector (SATA\_PWR1)



### 11 : SATA3 Connector (SATA3\_1)

### 12 : DC-In Jack (DC\_IN1)

### 13 : DisplayPort (DISPLAY1)

### 14 : Top : DisplayPort (DP2) Bottom : HDMI Port (HDMI1)

### 15 : Top : RJ-45 LAN Port (LAN2) Bottom : USB3.1 Port (USB3\_2)

### 16 : Top : RJ-45 LAN Port (LAN1) Bottom : USB3.1 Port (USB3\_1)

#### Back Side :

### Power Button (PWR\_BTN3)



### Fan Connector (FAN1)

